

11/27/01

Class	Subclass

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PATENT NUMBER

U.S. **UTILITY** Patent Application

<p><i>M. T</i></p> <p>O.I.P.E.</p> <p>SCANNED <u><i>TD3</i></u> Q.A. <u><i>AA</i></u></p>	PATENT DATE
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APPLICATION NO. 09/996869	CONT/PRIOR D	CLASS 118	SUBCLASS	ART UNIT 1763	EXAMINER
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## APPLICANTS

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**TITLE**

Semiconductor wafer processing system with vertically-stacked process chambers and single-axis dual-wafer transfer system

PTO-2040  
12/99

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
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<input type="checkbox"/> The terminal ____months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	
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